METHOD AND APPARATUS FOR
BASIC INPUT OUTPUT SYSTEM LOADING

ABSTRACT

A method and apparatus for basic input/output system (BIOS) loading includes a graphics processor performing a startup operation. The method and apparatus further includes this central processing unit (CPU) having a cache memory disposed therein. The method and apparatus provides for the graphic processor writing data to the cache memory prior to the execution of the startup operations such that the graphics processor may effectively and efficiently use the expanded memory resources of the cache memory prior to the CPU allocating and utilizing the CPU-specific resources. As such, the method and apparatus allows for improved BIOS loading by allowing for a greater degree of flexibility with an improved amount of available memory resources.